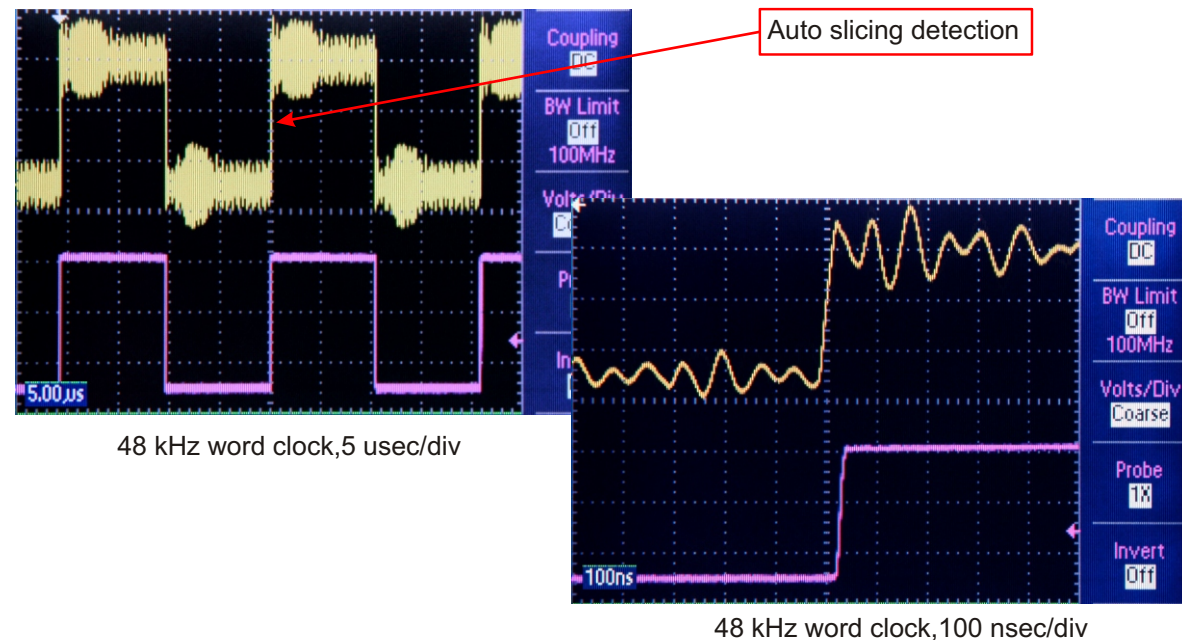
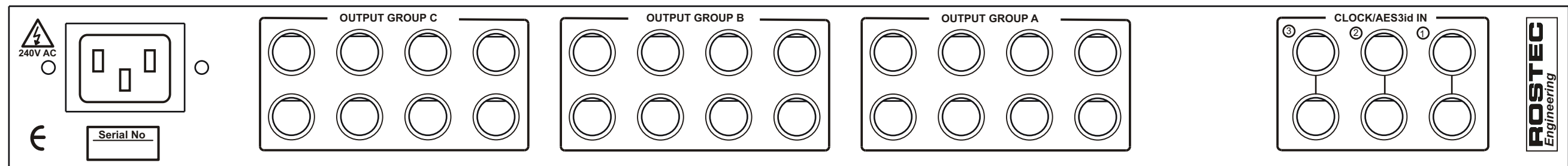
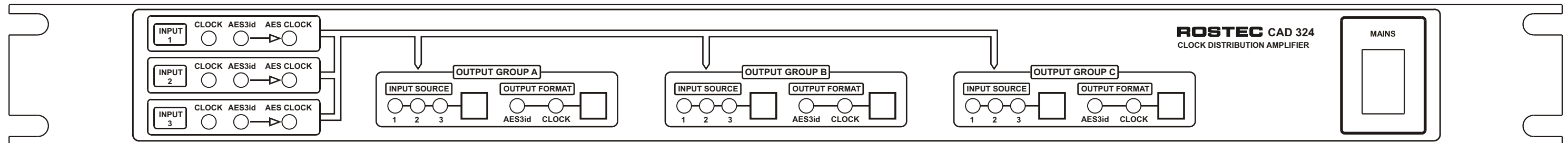


# ROSTEC CAD324 Clock/AES3id distribution amplifier

- 3 inputs into 4 output groups of 8 outputs each
- Clock distribution and AES3id distribution
- Inputs have auto slicing level detector for defining the "sweet spot" trigger point
- Highly sensitive balanced inputs. Accepts from 0.2 to 10 Volts PP input level.
- Extraction of word clock from the incoming AES3id signal, jitter typically 50 psec
- High clock bandwidth of 25 Mhz, AES3id sampling rates from 8 kHz to 192 kHz are supported
- Individual power supplies for each output group, eliminating jitter/noise crosstalk



**Cleaning-up of the incoming signal.**

The upper trace is an incoming Word clock at 48 kHz. The signal is totally cluttered with noise from neighboring signal sources and has lots of ringing due to wrong cable impedance or incorrect termination - generally all the bad things you would want to avoid in your installation.

CAD 324 automatically detects the "sweet spot" on the input where the signal is clean, as long as the input level is between 0.2 and 10 Volts pp. The result is a virtual perfect output signal, free from ringing and noise.